

51000 U.S. PTO
10/081915
02/22/02

P

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10081915	FILING DATE 02/22/2002	CLASS 438	SUBCLASS 129	GAU 2814 2825	EXAMINER W. Fahmy
**APPLICANTS: Gonzalez Fernando; Sandhu Gurtej; Violette Miko; <i>than Pham had parent</i>					
**CONTINUING DATA VERIFIED: THIS APPLICATION IS A DIV OF 09/570,614 05/12/2000 PAT 6,376,284 WHICH IS A DIV OF 08/604,751 02/23/1996					
BEST AVAILABLE COPY					
**FOREIGN APPLICATIONS VERIFIED:					
PG-PUB <input type="checkbox"/>		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiners's Initials.			ATTORNEY DOCKET NO MCRO:125-4/FLE 94-0281.0		
TITLE : Method for forming conductors in semiconductor devices					
<small>U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)</small>					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Assistant Examiner	
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Total Claims	
		Print Claim for O.G.	
		Sheets Drwg. Figs. Drwg. Print Fig.	
		Primary Examiner	
		Application Examiner	
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	
WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.			

FILED WITH:

☐ DISK (CRF)

☐ CD-ROM

(Attached in pocket on right inside flap)